

COMPANY FACT SHEET



BACKGROUND

Tanner EDA is a leading provider of electronic design automation (EDA) software solutions for the design, layout and verification of Analog/Mixed-Signal ICs and MEMS. Its tools help automate and simplify the design process, enabling engineers to cost-effectively bring commercially successful electronic products to market ahead of the competition.

Tanner EDA is a business unit of Tanner Research. It was founded in 1988 by Dr. John Tanner as a means to develop and market cost-effective, easy-to-use EDA tools. Tanner had previously founded Stac Electronics, a company responsible for the introduction of a patented disk-doubler technology. Over the years, Tanner's extensive experience with ICs and other electronic components made it readily apparent that few flexible, affordable tools existed for the creation of innovative new products, thereby motivating the creation of Tanner EDA tools.

Tanner EDA employs 75 people and has experienced consistent annual growth of more than 25 percent. It is one of the largest software companies in the Los Angeles area and has received a number of awards, including the regional Deloitte & Touche Fast 50 list of companies.

Tanner Research's other business unit, Tanner Laboratories, conducts advanced research and development under government contract with an emphasis on image processing and micro-electromechanical systems (MEMS) design and fabrication. Tanner Laboratories also has fabrication facilities for MEMS and other devices. Another business unit, Manuflex was formed to concentrate on the design and low-volume supply of high-performance ICs. It specializes in the design and manufacture of analog and digital custom silicon solutions.

PRODUCTS

Tanner's fully-integrated solutions consist of tools for schematic entry, circuit simulation, waveform probing, full-custom layout editing, placement and routing, netlist extraction, LVS and DRC verification. Tanner EDA's innovative solutions are used in a range of applications in next-generation wireless, consumer electronics, imaging, power management, biomedical, automotive and RF market segments.

Tanner's S-Edit™ schematic capture tool, has been completely re-architected and rebuilt into a new tool with user interface, performance and interoperability enhancements added. S-Edit supports integrated analog simulation with automatic conversion from Cadence® and ViewDraw® schematics. Users can run simulations and cross-probe from S-Edit, making the design process real-time and more efficient. The ability to view operating point simulation results directly on the schematic is another S-Edit productivity enhancing feature, plus multiple libraries and language support for English, Chinese, Russian and Japanese, all combine to deliver a comprehensive and interactive design environment.

The T-Spice™ Circuit Simulator product delivers highly accurate results by supporting the latest foundry models, along with state-of-the-art numerical methods. T-Spice offers options and commands not found in Berkeley SPICE or most derivatives, such as design optimization, Monte Carlo analysis, multi-dimensional parameters, source and temperature sweeping. Tightly integrated with Tanner EDA's S-Edit schematic capture tool, T-Spice is tightly integrated with Tanner EDA's S-Edit schematic capture tool.

Tanner's L-Edit™ tools for physical design product features integrated solutions for layout editing, verification, placement and routing of Analog/Mixed-Signal ICs and MEMS. Its intuitive and customizable interface minimizes the user's learning curve. Plus its fast rendering – the fastest on the market – significantly speeds the design cycle.

The HiPer Verify® product automates design rule checking (DRC) for deep submicron manufacturing. Foundry-compatible rule support allows it to run Calibre® and Dracula® foundry files natively, without conversion or modification. Plus it tightly integrates into the L-Edit environment, allowing design rule violations to be identified and repaired early before they become a major problem.

APPLICATIONS

Tanner EDA tools are used by more than 25,000 engineers for Analog/Mixed-Signal ICs and MEMS devices. Customers such as Catalyst Semiconductor, Honeywell, Jet Propulsion Laboratory, NEC, Ricoh Company Ltd., Sarnoff Corporation, Xerox Corporation and others rely on these tools to help them speed from concept to silicon efficiently. Some of the products designed with Tanner EDA tools include imaging technology for the Mars Rover, components for Bluetooth peripherals and thermal management sensors for cell phones and notebook PCs.

LOCATION

Tanner EDA is headquartered in Monrovia, Calif. It sells its products directly in North America, Japan and Taiwan, and through distributors in Europe, Singapore, Malaysia, China, Korea, India, and the Middle East.

LEADERSHIP

Dr. John Tanner, president and chief executive officer
Linda Tanner, vice president
Kevin Dinniene, Controller
Daniel Hamon, general manager
Dr. Massimo Sivilotti, chief scientist and technology officer

FUNDING

The company is privately owned by its employees.

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