

**TANNER EDA ANNOUNCES ITS LATEST T-SPICE PRO SUPPORTS LEADING
EDGE 65 NANOMETERS AND BELOW TRANSISTOR MODELS**

**T-Spice Pro v11.2 Includes Support for the Penn State Philips (PSP) Model
Chosen By Compact Model Council (CMC) and Other New Features**

MONROVIA, Calif., Feb. 6, 2006 – Tanner EDA today announced that the newest version of its T-Spice Pro Circuit Simulator program for analog and mixed-signal integrated circuit design supports the transistor model recently selected as the next standard for simulating future CMOS transistors manufactured at 65 nanometers and below. The Penn State Philips (PSP) model was selected by the Compact Model Council, an industry group of 31 semiconductor and circuit simulator suppliers that focuses on the standardization, implementation and use of transistor models. The CMC tested several advanced models before voting to adopt PSP.

PSP will succeed the BSIM3 and BSIM4 models currently in use. It is anticipated that PSP will help simplify the exchange of chip design information and support more accurate digital, analog and mixed-signal circuit behavior analysis.

“Tanner’s rapid support of the PSP model demonstrates our ongoing commitment to adopting the latest technologies and enabling our customers to adopt leading-edge industry standards,” noted Dr. John Tanner, president and CEO, Tanner Research.

Based on an intuitive graphical user interface that runs on Windows-based systems, T-Spice Pro’s table-based and direct modeling enables fast simulation of complex circuits. Key features include device state plotting, real-time waveform viewing and analysis and command tools for simpler SPICE syntax creation. T-Spice Pro also will continue to support other industry-standard models including MOSFET and others and now supports the latest BSIM3.3 and BSIM4.5 releases. Two stress effect models, from the Berkeley BSIM4 model and from TSMC processes, have been added to the BSIM3 model to provide more accuracy in smaller geometry processes.

Tanner’s S-Edit front-end design tools are included in T-Spice Pro for schematic entry; waveform and operating point cross-probing; portable data files; support for industry file outputs; component rotation, inversion and reflection; and drag-and-drop device placement.

-- more --

About Tanner EDA

Tanner EDA is a leading provider of easy-to-use, PC-based electronic design automation (EDA) software solutions for the design, layout and verification of analog/mixed-signal integrated circuits, ASICs and MEMS. Its solutions help speed designs from concept to silicon and are used by thousands of companies to develop devices cost-effectively in the biomedical, consumer electronics, next-generation wireless, imaging, power management and RF market segments. Founded in 1988, Tanner EDA is a division of privately held Tanner Research, Inc. For more information, go to www.tannereda.com.

-- 30 --

All brands and trademarks are the property of their respective owners.