

**TANNER'S L-EDIT IC DESIGN TOOL GETS NEW CAPABILITIES TO SPEED DESIGN AND TIME TO SILICON FOR ANALOG, MIXED-SIGNAL AND MEMS DEVICES**

**PASADENA**, Calif., Nov. 14 2005 – Design tools provider Tanner EDA today announced new capabilities for its Tanner Tools L-Edit® program for layout, verification, and placement and routing of analog and mixed-signal integrated circuits, ASICs and MEMS.

L-Edit accelerates the design cycle from concept to silicon by providing a complete package of layout tools with an intuitive and programmable interface. L-Edit 11.2 adds several key features to improve productivity, automate tedious manual design tasks, and make designing faster. These include:

- Design rule checking (DRC) that runs 30 percent faster than previous versions of the program.
- Multi-grid toolbar feature that allows users to switch easily among various grid settings, enabling faster layout and manual routing that is DRC correct.
- Automatic guard ring generation, automatic via placement during manual routing, and via array generation allowing significantly faster analog layout. Guard rings and via can be set-up through L-Edit's user interface or imported from a Virtuoso® technology file.
- Clip-out region feature that allows users to instantly copy the layout in a specified rectangle for fast changes to designs.
- Ability to import bitmaps or to place geometric text using any font directly in the layout, which allows designers to add text or logos.

Tanner Tool's HiPer® Verify, which automates design rule checking for deep submicron manufacturing, also has been enhanced to support connectivity-based DRC rules. Foundry-compatible rule support allows it to run Calibre® and Dracula® foundry files natively, without conversion or modification.

**About Tanner EDA**

Tanner EDA is a leading provider of easy-to-use, PC-based electronic design automation (EDA) software solutions for the design, layout and verification of analog/mixed-signal integrated circuits, ASICs and MEMS. Its solutions help speed designs from concept to silicon and are used by thousands of companies to develop devices cost-effectively in the biomedical, consumer electronics, next-generation wireless, imaging, power management and RF market segments. Founded in 1988, Tanner EDA is a division of privately held Tanner Research, Inc. For more information, go to [www.tannereda.com](http://www.tannereda.com).