



**ANSEM DEVELOPS ADVANCED ANALOG IC DESIGN METHODOLOGY
BASED ON TANNER TOOLS**

PASADENA, California -- January 19, 2001 -- With a name that suggests their expertise in analog semiconductors, AnSem develops advanced analog circuits and systems for telecommunication applications. Known for their ability to design analog ICs in whatever fabrication technology their clients require, AnSem provides complete chip and IP design services for the mixed-signal telecomm industry.

AnSem has developed a unique design methodology that renders better performance and faster design cycles compared to standard approaches. The company has created a layer of analog synthesis and layout generation software atop Tanner EDA products for better automation of the design process. "We use every component in the Tanner Tools Pro package," commented Stefan Gogaert, General Manager. "We use S-Edit for schematic entry, T-Spice and W-Edit for simulation, and L-Edit for layout, DRC, and LVS." In addition to Tanner software, AnSem has assembled an impressive library of UPI macros for layout generation of custom components, including on-chip spiral inductors. "We make extensive use of the L-Edit UPI to further automate, customize, and accelerate our design process. This allows us to turn around custom analog IC designs as efficiently as possible."

AnSem has used their custom methodology to design complete chips that include a fully integrated single-chip CMOS wireless receiver system that can be realized in most 0.35 um process technologies. Operational above 1 GHz, the design includes a complete synthesizer (with loop filter), baseband filter, ADC, and integrated inductors and variable capacitors for its LNA and VCO.

Tanner EDA Background

Tanner EDA is a division of Tanner Research, Inc., a privately held company with corporate offices in Pasadena, California. Tanner EDA was the first provider of custom IC layout, verification, and simulation software for the PC. Specializing in innovative EDA

software for analog and mixed-signal IC design, Tanner's Windows® products are known for their affordability, ease-of-use, unique layout crafting support, simulation technology, and customization capabilities. Tanner Tools are popular with analog, RFIC, mixed-signal, imaging array, memory, and power chip designers of cells, cores, IP blocks, and complete integrated circuits. With over 12,000 licenses worldwide, many designers rely exclusively on Tanner Tools. More information about Tanner EDA, its products and services may be found at www.tannereda.com.

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