



**TANNER EDA PRODUCTS MAKE A SPLASH AT DAC AND THE CUSTOM  
INTEGRATED CIRCUITS CONFERENCE**

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PASADENA, Calif., June 19, 2000 - Tanner EDA, the leading supplier of integrated circuit design tools for PC platforms, had spectacular successes at both DAC held in Los Angeles (June 5-8) and the Custom Integrated Circuits Conference held in Orlando (May 22-24). Large crowds of interested customers stopped by the Tanner EDA booth to listen to product demonstrations about desktop solutions for their ASIC design processes.

A new workshop for "VLSI Design Educators" was introduced at DAC this year, and Tanner Tools were highly recommended for use in the educational marketplace. Cherrice Traver, Professor of Electrical Engineering and Computer Science at Union College in Schenectady, New York, emphasized the value and importance of using Tanner Tools in her course. You can view Traver's power point presentation at this year's DAC convention titled "VLSI Design Using PC-Based Tools." "I use the tools because the tool flow is straightforward and it matches the ASIC standard cell approach that fits in my course. Tanner Tools are easy to install and maintain, and the interface is somewhat intuitive for students," explained Professor Traver, "and the Tanner libraries also provide an easy interface to fabrication through MOSIS." The main emphasis of the workshop was to expose educators to new course material and design tools so they can prepare future VLSI design engineers to meet the challenges of recent developments in the design automation industry.

At the two tradeshow, Tanner EDA hosted a 'Lucky Draw' contest to giveaway a Palm Pilot™ to the lucky winners. Design engineers from Tower Semiconductor, TDK, and Starburst Technical were chosen to receive the prizes. Please look at our website for the names of the winners at [www.tanner.com/eda](http://www.tanner.com/eda). Tanner EDA will be back at DAC 2001 in Las Vegas and CICC in San Diego, so we hope to see all design engineers there.

### **About Tanner EDA**

Tanner EDA is a division of Tanner Research, Inc., a privately held company with worldwide distribution and offices located in both Pasadena and Sunnyvale, California. Tanner EDA was the first solutions provider on the PC platform to develop layout, verification, and simulation software for analog and mixed-signal IC designs. With over 12,000 licenses in more than 35 countries, many designers rely exclusively on Tanner Tools to meet their design needs. For more information about Tanner EDA please visit our website [www.tannereda.com](http://www.tannereda.com).

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